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Biography:

H.-S. Philip Wong is the Willard R. and Inez Kerr Bell Professor in the School of Engineering. He joined Stanford University as Professor of Electrical Engineering in September, 2004. From 1988 to 2004, he was with the [IBM T.J. Watson Research Center](#).

At IBM, he held various positions from Research Staff Member to Manager and Senior Manager. While he was Senior Manager, he had the responsibility of shaping and executing IBM's strategy on nanoscale science and technology as well as exploratory silicon devices and semiconductor technology.

Professor Wong's research aims at translating discoveries in science into practical technologies. His works have contributed to advancements in nanoscale science and technology, semiconductor technology, solid-state devices, and electronic imaging.

He is a Fellow of the IEEE. He served as the Editor-in-Chief of the [IEEE Transactions on Nanotechnology](#) in 2005 – 2006, sub-committee Chair of the [ISSCC](#) (2003 – 2004), General Chair of the [IEDM](#) (2007), and is currently the Chair of the IEEE Executive Committee of the [Symposia of VLSI Technology and Circuits](#). He is the founding Faculty Co-Director of the [Stanford SystemX Alliance](#) – an industrial affiliate program focused on building systems.